

## CLAIMS

1. A circuit to detect pin layer reversal, comprising:
  - an input circuit to receive an input signal having a first portion to indicate a pin layer reversal and having a second portion to indicate a servo sync mark;
  - 5 a first servo sync mark detector for detecting a positive servo sync mark from said input signal;
  - a second servo sync mark detector for detecting a negative servo sync mark from said input signal; and
  - 10 a circuit responsive to said positive servo sync mark and said negative servo sync mark to generate a signal to indicate if said servo sync mark has been reversed and to generate a signal to indicate said pin layer reversal.
2. A circuit to detect pin layer reversal, as in Claim 1, wherein said circuit responsive to said positive and negative servo sync mark includes an exclusive OR circuit.
3. A circuit to detect pin layer reversal, as in Claim 1, wherein said circuit responsive to said positive and negative servo sync mark includes an AND circuit.